

<b>INFORMATION DISCLOSURE CITATION</b> <b>ON AN APPLICATION</b>  <del>SEARCHED</del> <del>INDEXED</del> <del>MAILED</del> (Nov 28 2007) (Use several sheets if necessary)		Attorney Docket No.: <b>8006-1026</b>		Application No.: <b>10/700,451</b>	
		Applicant: <b>Hidefumi NATSUME et al.</b>			
		Filing Date: <b>November 5, 2003</b>		Group Art Unit: <b>2619</b>	
<b>U.S. PATENT DOCUMENTS</b>					
Examiner Initial	Document Number	Date	Name	Class	Subclass
<b>FOREIGN PATENT DOCUMENTS</b>					
Examiner Initial	Document Number	Date	Country	Class	Subclass
Ru	JP 11-331237	11/30/1999	JAPAN (with English abstract)		
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>					
	HIDAKA et al., "Proposal of Next Generation Ethernet Architecture: GOE", Collected Papers for Presentations 2 at 2002 Communications Society Conference by The Institute of Electronics, Information and Communication Engineers, B-7-11, August 20, 2002, pp. 171.				
	MIYASAKA, "Dynamic VLAN System Using Tag Conversion Equipment", Collected Papers for Presentations 2 at 2002 General Conference by The Institute of Electronics, Information and Communication Engineers, B-6-74, March 7, 2002, pp. 88.				
EXAMINER: <i>Ron Abelson</i>		DATE CONSIDERED <i>2/29/08</i>			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.					

\* Abstract provided for the Examiner's convenience